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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Brian G. Johnson

§ Group Art Unit: 2827

Serial No.: 10/634,146

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Filed: August 4, 2003

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Examiner: Michael J. Weinberg

For: Optically Accessible Phase Change
Memory

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Atty. Dkt. No.: ITO.0549US (P16246)

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant submits the reference listed on the attached form PTO 1449 together with any required copies of such reference.

This statement is being filed after a first Office Action on the merits, but before receipt of a final Office action or a Notice of Allowance. A check for \$180 in payment of the late submission fee of §1.17(p) is enclosed. Please apply any additional charges or credits to Deposit Account No. 20-1504 (ITO.0549US).

Respectfully submitted,

Date: 11/6/06

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11/08/2006 EAYALEW1 00000035 10634146

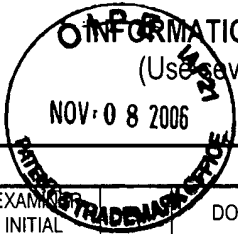
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180.00 OP

Date of Deposit: November 6, 2006

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Sherry Tipton

				ATTY DOCKET NO. ITO.0549US (P16246)		SERIAL NO. 10/634,146	
				APPLICANT(S): BRIAN G. JOHNSON			
				FILING DATE: August 4, 2003		GROUP ART UNIT: 2827	
U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
A.							
B.							
C.							
D.							
E.							
F.							
FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO	
G.							
H.							
I.							
J.							
K.							
L.							
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
M.	Horii, H., Yi, J.H., Park, J.H., Ha, Y.H., Baek, I.G., Park, S.O., Hwang, Y.N., Lee, S.H., Kim, Y.T., Lee, K.H., Chung, U-In and Moon, J.T., "A Novel Cell Technology Using N-doped GeSbTe Films for Phase Change RAM," presented at IEEE 2003 Symposium on VLSI Technology, Kyoto, Japan, June 12-14, 2003						
N.							
O.							
P.							
Q.							
R.							
S.							
EXAMINER				DATE CONSIDERED			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.